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... The main feature of UMPS is the ability to build a **virtual microcontroller** board on screen by connecting various virtual resources (switches, LEDs, ...

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... These challenges are intended to encourage you to experiment both with the **virtual microcontroller** and miSim DE itself. Can you rise to the challenge? ...

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... For example, you can integrate a PCI interface with a **virtual microcontroller** core, all on a single FPGA. Before the advent of hardware definition ...

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... Thus we have included **virtual microcontroller**. modules (eg for the 16 bit C167

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PICLAB 2.1 - readme.txt ===== Product Name ...  
 ... with good correspondence between the operation of the **virtual microcontroller** on the PC screen and the physical microcontroller on the bench. ...  
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 ... the **virtual microcontroller** in your PC. The Keil debugger tScope will communicate with the Intel. RISM monitor contained in the EPROM on the USB board. ...  
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 ... Individual cocoon, as said before, looks like a **virtual microcontroller** having its own logic and data flows. associated with it. ...  
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 ... The main feature of UMPS is the ability to build a **virtual microcontroller** board on screen by connecting various virtual resources (switches, LEDs, displays, A ...  
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 ... The EMIT Access Library software tools allow development of custom control applications. The C EMIT Device simulator, emMicro on a **virtual microcontroller**. ...  
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 ... effective when applied to data that are fully sampled in all three dimensions umps is now \$35 umps is the ability to build a **virtual microcontroller** board on ...  
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... in. P-57 Mustang board, power supply, and RS-232C cable. The C compiler  
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*Bernard Cole. Electronic Engineering Times. Manhasset: Apr 9, 2001. p. 91 (4 pages)*

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*Anonymous. Embedded Systems Development.* Cleveland: Aug 2000. Vol. 3, Iss. 8; p. 37 (2 pages)

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









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- ☐ 19. **Scenix Semiconductor Adds Director of Operations; Veteran Engineer and Manager Named To Lead Major Production Ramp For SX Series Microcontrollers**  
*Business Editors/High-Tech Writers. Business Wire. New York: Jun 2, 1999. p. 1*  
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*Andrew MacLellan. Electronic Buyers' News. Aug 31, 1998. p. PG.22*  
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3 ICEBERG: an embedded in-circuit emulator synthesizer for microcontrollers

*Ing-Jer Huang; Tai-An Lu;*

Design Automation Conference, 1999. Proceedings. 36th , 21-25 June 1999

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*Eriksen, C.C.; Osse, T.J.; Light, R.D.; Wen, T.; Lehman, T.W.; Sabin, P.L.;*

*Ballard, J.W.; Chiodi, A.M.;*

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**5 The second NASA/DoD workshop on evolvable hardware [Book Review]**

*de Garis, H.;*

Evolutionary Computation, IEEE Transactions on , Volume: 5 , Issue: 3 , June 2001

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*Helal, S.;*

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**10 On communication security in wireless ad-hoc sensor networks**

*Slijepcevic, S.; Potkonjak, M.; Tsiatsis, V.; Zimbeck, S.; Srivastava, M.B.;*

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**11 Noise-enhanced human sensorimotor function**

*Collins, J.J.; Priplata, A.A.; Gravelle, D.C.; Niemi, J.; Harry, J.; Lipsitz, L.A.;*

Engineering in Medicine and Biology Magazine, IEEE , Volume: 22 , Issue: 2 , March-April 2003

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**12 SystemC cosimulation and emulation of multiprocessor SoC designs**

*Benini, L.; Bertozzi, D.; Bruni, D.; Drago, N.; Fummi, F.; Poncino, M.;*  
Computer , Volume: 36 , Issue: 4 , April 2003  
Pages:53 - 59

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**13 NASA advances robotic space exploration**

*Katz, D.S.; Some, R.R.;*  
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[\[Abstract\]](#) [\[PDF Full-Text \(299 KB\)\]](#) **IEEE JNL**

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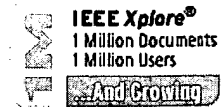
*Thane, H.; Sundmark, D.; Huselius, J.; Pettersson, A.;*  
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*Kaenel, R.;*

Audio and Electroacoustics, IEEE Transactions on , Volume: 18 , Issue: 4  
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**3 Implementation of a Multirate Speech Digitizer**

*Byung Lee; Chong Un; Hyeong Lee; Byoung Shin; Hwang Lee;*

Communications, IEEE Transactions on [legacy, pre - 1988] , Volume: 31  
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*Feldman, J.A.; Hofstetter, E.M.; Malpass, M.L.;*

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**9 Hardware/software co-design**

*De Michell, G.; Gupta, R.K.;*

Proceedings of the IEEE , Volume: 85 , Issue: 3 , March 1997

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